

EAST - [thinsearch15.wsp:1]

FileViewEditToolsWindowHelp

Pending

Active

L1: (4) "6483100"

L2: (36) "5091759"

L3: (5) "5284782"

L4: (23) "5463978"

L5: (27) "5492857"

L6: (8) "6057566"

L7: (14) "5013685"

L8: (13) "5324682"

L9: (4) "6025613"

L10: (3) "6258616"

L11: (13) "5488237"

L12: (8) "6380569"

L13: (142) 1 2 3 4 5 6 7 8 9 10 11 12

L14: (2463293) alternat56

L15: (76) 13 and 14

L16: (820716) insulating

L17: (46) 15 and 16

L18: (2) "6690035"

Failed

USPAT: US POPUB: EPO: IPO: DERVENT: IBM: 100

Default operator: OR

"6690035"

6/16/04

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Image

Text

HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cla	Inventor	S	C	P	2	3	Image Doi
1	<input type="checkbox"/>	<input type="checkbox"/>	US 6690035 B1	20040210	38	Semiconductor device having an active region of alternating layers	257/77	257/194		Yokogawa, Toshiya et al.	<input checked="" type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 669003
2	<input checked="" type="checkbox"/>	<input type="checkbox"/>	WO 200167521 A	20040517		Semiconductor device includes thick undoped layers and thin layers alterna				KITABATAKE, M et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	

Ready

NUM

DB: USPAT; US PGPUB; EPO; JPO; DERWENT; IBM; TOS

Default operator: OR

32 AND 7

6/15/04

 BBS form
  IS&R form
  Image
  Text
  HTML

	U	I	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Cls	Inventor	S	C	P	2	3	Image 1*
1	<input type="checkbox"/>	<input type="checkbox"/>	US 20030222312 A1	20031204	16	Planar polymer transistor	257/347	257/E51.007		Breen, Tricia L. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2003
2	<input type="checkbox"/>	<input type="checkbox"/>	US 20030134487 A1	20030717	12	METHOD OF FORMING A PLANAR POLYMER TRANSISTO	438/455	257/E51.007; 438/151		Breen, Tricia L. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 2003
3	<input type="checkbox"/>	<input type="checkbox"/>	US 6620657 B2	20030916	12	Method of forming a planar polymer transistor using substrate bonding tec	438/151	257/E51.007; 438/164		Breen, Tricia L. et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6620
4	<input type="checkbox"/>	<input type="checkbox"/>	US 5281543 A	19940125	18	Fabrication method for quantum devices in compound semiconductor	438/186	257/14; 257/22		Fukuzawa, Tadashi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 5281
5	<input type="checkbox"/>	<input type="checkbox"/>	US 6170736 A	19991108	18	Fabrication of quantum devices in	257/14	257/22		Fukuzawa, Tadashi et al.	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	<input type="checkbox"/>	US 6170